

## Abstract

A carry-save adder for adding bits of the same weight comprises six inputs ( $I_0, I_1, \dots, I_5$ ) for receiving six bits of in each case the same weight  $w$ , to be added. The adder has an output ( $S$ ) for a sum bit of weight  $w$  and two outputs ( $C_0, C_1$ ) for two carry bits of weights  $2w$  and  $4w$ .